



## 6-Pin DIP Zero-Cross Optoisolators Triac Driver Output (600 Volts Peak)

The MOC3162 and MOC3163 devices consist of gallium arsenide infrared emitting diodes optically coupled to monolithic silicon detectors performing the functions of Zero Voltage Crossing bilateral triac drivers.

They are designed for use with a triac in the interface of logic systems to equipment powered from 115/240 Vac lines, such as solid-state relays, industrial controls, motors, solenoids and consumer appliances, etc.

- Simplifies Logic Control of 115/240 Vac Power
- Zero Voltage Turn-On
- $dv/dt$  of 1000 V/ $\mu$ s Guaranteed Minimum @ 600 V Peak
- $I_{FT}$  Insensitive to Static  $dv/dt$  (Within Rated  $V_{DRM}$ )
- **To order devices that are tested and marked per VDE 0884 requirements, the suffix "V" must be included at end of part number. VDE 0884 is a test option.**

### Recommended for 115/240 Vac(rms) Applications:

- Solenoid/Valve Controls
- Lighting Controls
- Static Power Switches
- AC Motor Drives
- Static AC Power Switch
- Temperature Controls
- E.M. Contactors
- AC Motor Starters
- Solid State Relays

### MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
<b>INFRARED EMITTING DIODE</b>			
Reverse Voltage	$V_R$	6.0	Volts
Forward Current — Continuous	$I_F$	60	mA
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ Negligible Power in Output Driver Derate above $25^\circ\text{C}$	$P_D$	120	mW
		1.60	mW/ $^\circ\text{C}$

### OUTPUT DRIVER

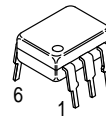
Off-State Output Terminal Voltage	$V_{DRM}$	600	Volts
Peak Repetitive Surge Current ( $PW = 100 \mu\text{s}, 120 \text{ pps}$ )	$I_{TSM}$	1.0	A
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	150	mW
		2.0	mW/ $^\circ\text{C}$

### TOTAL DEVICE

Isolation Surge Voltage (1) (Peak ac Voltage, 60 Hz, 1 Second Duration)	$V_{ISO}$	7500	Vac(pk)
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	250	mW
		3.3	mW/ $^\circ\text{C}$
Junction Temperature Range	$T_J$	-40 to +100	$^\circ\text{C}$
Ambient Operating Temperature Range	$T_A$	-40 to +85	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	-40 to +150	$^\circ\text{C}$
Soldering Temperature (10 s)	$T_L$	260	$^\circ\text{C}$

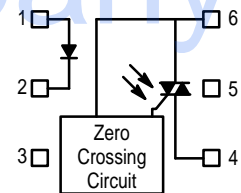
1. Isolation surge voltage,  $V_{ISO}$ , is an internal device dielectric breakdown rating. For this test, Pins 1 and 2 are common, and Pins 4, 5 and 6 are common.

**MOC3162**  
**MOC3163**



STANDARD THRU HOLE

### COUPLER SCHEMATIC



1. ANODE
2. CATHODE
3. NC
4. MAIN TERMINAL
5. SUBSTRATE  
DO NOT CONNECT
6. MAIN TERMINAL

**ELECTRICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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**INPUT LED**

Reverse Leakage Current ( $V_R = 6.0\text{ V}$ )	$I_R$	—	0.05	100	$\mu\text{A}$
Forward Voltage ( $I_F = 30\text{ mA}$ )	$V_F$	—	1.15	1.5	Volts

**OUTPUT DETECTOR** ( $I_F = 0$ )

Leakage with LED Off, Either Direction (Rated $V_{DRM}$ , Note 1)	$I_{DRM}$	—	10	100	nA
Critical Rate of Rise of Off-State Voltage (Note 3) @ 600 V Peak	dv/dt	1000	—	—	V/ $\mu\text{s}$

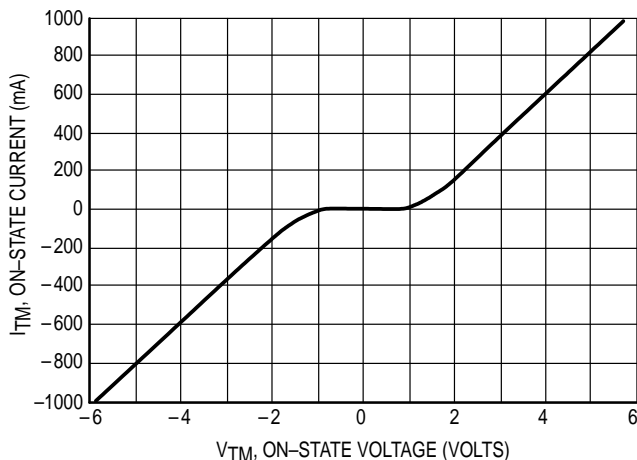
**COUPLED**

LED Trigger Current, Current Required to Latch Output (Main Terminal Voltage = 3.0 V, Note 2)	$I_{FT}$	—	—	10	mA
		—	—	5.0	
Peak On-State Voltage, Either Direction ( $I_{TM} = 100\text{ mA Peak}$ , $I_F = \text{Rated } I_{FT}$ )	$V_{TM}$	—	1.7	3.0	Volts
Holding Current, Either Direction	$I_H$	—	200	—	$\mu\text{A}$
Inhibit Voltage (MT1–MT2 Voltage Above Which Device Will Not Trigger) ( $I_F = \text{Rated } I_{FT}$ )	$V_{INH}$	—	8.0	15	Volts
Leakage in Inhibited State ( $I_F = 10\text{ mA Maximum}$ , at Rated $V_{DRM}$ , Off State)	$I_{DRM2}$	—	250	500	$\mu\text{A}$

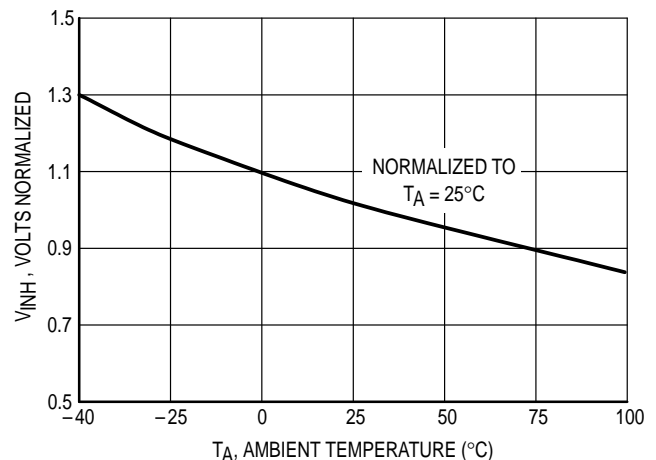
1. Test voltage must be applied within dv/dt rating.
2. All devices are guaranteed to trigger at an  $I_F$  value less than or equal to max  $I_{FT}$ . Therefore, recommended operating  $I_F$  lies between max  $I_{FT}$  (10 mA for MOC3162, 5.0 mA for MOC3163) and absolute max  $I_F$  (60 mA).
3. This is static dv/dt. See Figure 9 for test circuit. Commutating dv/dt is a function of the load-driving thyristor(s) only.

**TYPICAL ELECTRICAL CHARACTERISTICS**

$T_A = 25^\circ\text{C}$



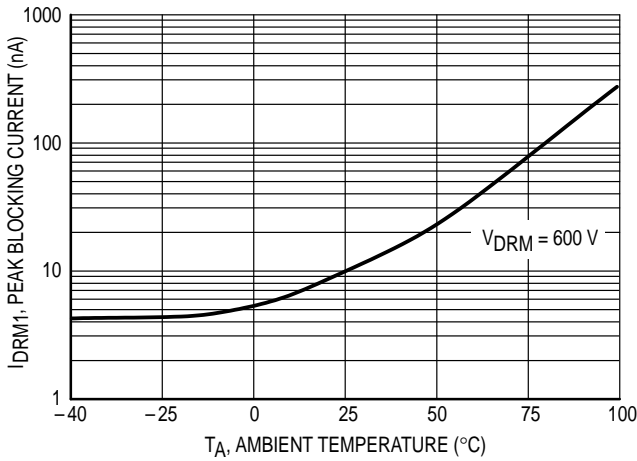
**Figure 1. On-State Characteristics**



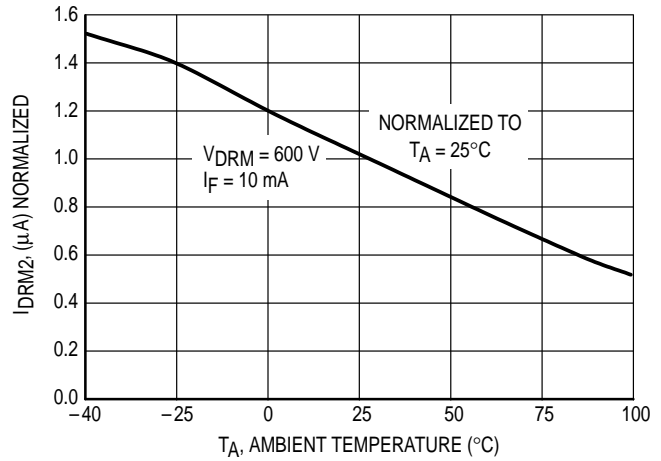
**Figure 2. Inhibit Voltage versus Temperature**

TYPICAL ELECTRICAL CHARACTERISTICS

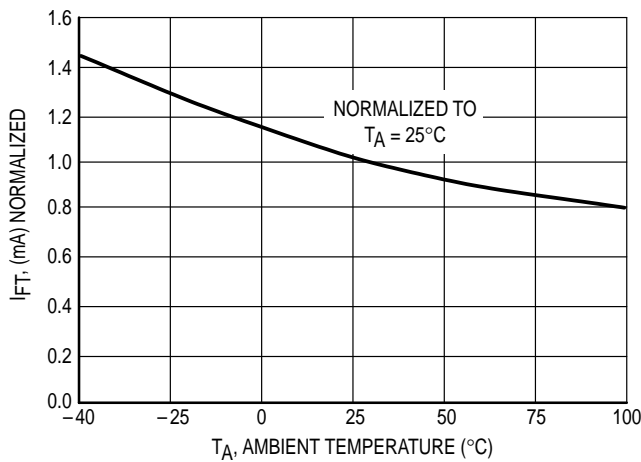
$T_A = 25^\circ\text{C}$



**Figure 3. Leakage with LED Off versus Temperature**



**Figure 4. IDRM2, Leakage in Inhibit State versus Temperature**



**Figure 5. Trigger Current versus Temperature**

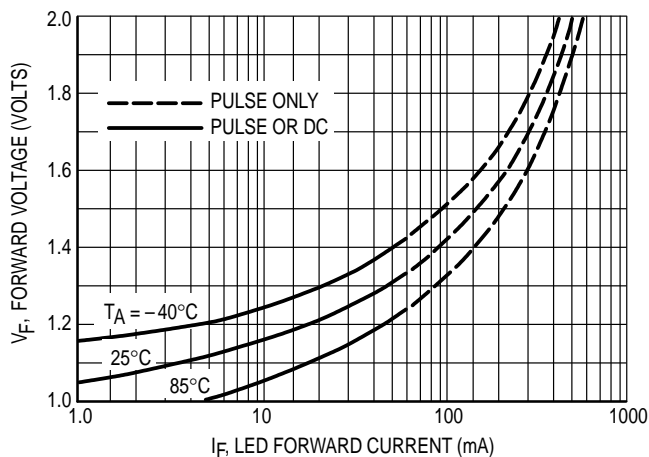
**IFT versus Temperature (Normalized)**

This graph shows the increase of the trigger current when the device is expected to operate at an ambient temperature below 25°C. Multiply the normalized IFT shown on this graph with the data sheet guaranteed IFT.

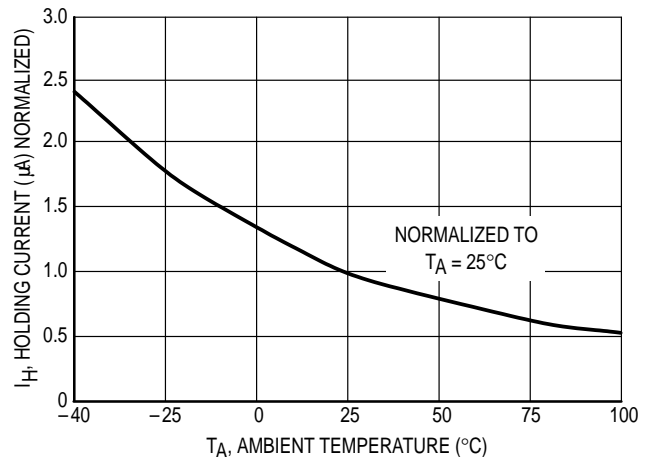
Example:

$$T_A = -40^\circ\text{C}, I_{FT} = 10 \text{ mA}$$

$$I_{FT} @ -40^\circ\text{C} = 10 \text{ mA} \times 1.4 = 14 \text{ mA}$$



**Figure 6. LED Forward Voltage versus Forward Current**



**Figure 7. Holding Current, IH versus Temperature**

TYPICAL ELECTRICAL CHARACTERISTICS

T<sub>A</sub> = 25°C

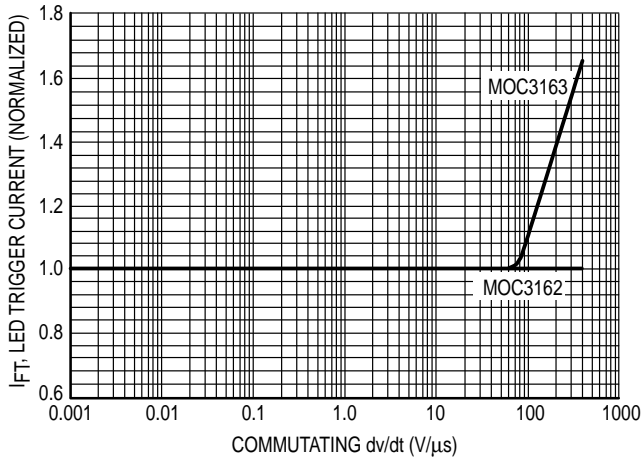


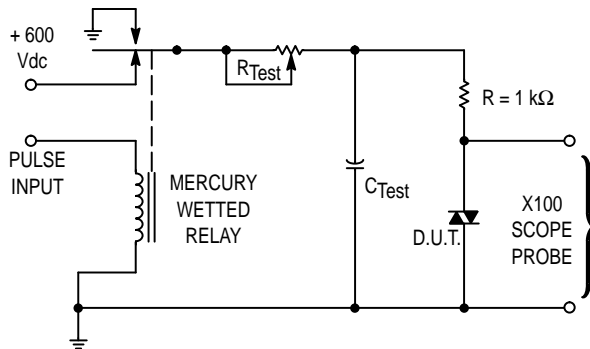
Figure 8. LED Trigger Current, I<sub>FT</sub>, versus dv/dt

I<sub>FT</sub> versus dv/dt

Triac drivers with good noise immunity (dv/dt stat.) have internal noise rejection circuits which prevent false triggering of the device in the event of fast rising line voltage transients. Inductive loads generate a commutating dv/dt that may activate the triac driver's noise suppression circuits. This prevents the device from turning on at its specified trigger current. It will in this case go into the mode of "half-waving" of the load. Half-waving of the load may destroy the power triac and the load.

Figure 8 shows the dependency of the triac drivers I<sub>FT</sub> versus the reapplied voltage rise with a V<sub>p</sub> of 600 V. This dv/dt condition simulates a worst case commutating dv/dt amplitude.

It can be seen that the required trigger current I<sub>FT</sub> changes with increased dv/dt. Practical loads generate a commutating dv/dt of less than 50 V/μs. The rate of rise of the commutating dv/dt is effectively slowed by the use of snubber networks across the main triac. This snubber is also needed to keep the commutating dv/dt generated by inductive loads within the commutating dv/dt ratings of the power triac.



1. The mercury wetted relay provides a high speed repeated pulse to the D.U.T.
2. 100x scope probes are used, to allow high speeds and voltages.
3. The worst-case condition for static dv/dt is established by triggering the D.U.T. with a normal LED input current, then removing the current. The variable R<sub>TEST</sub> allows the dv/dt to be gradually increased until the D.U.T. continues to trigger in response to the applied voltage pulse, even after the LED current has been removed. The dv/dt is then decreased until the D.U.T. stops triggering. τ<sub>RC</sub> is measured at this point and recorded.

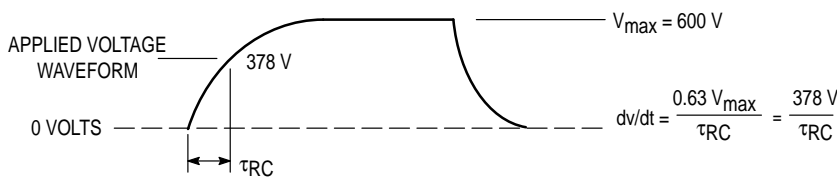
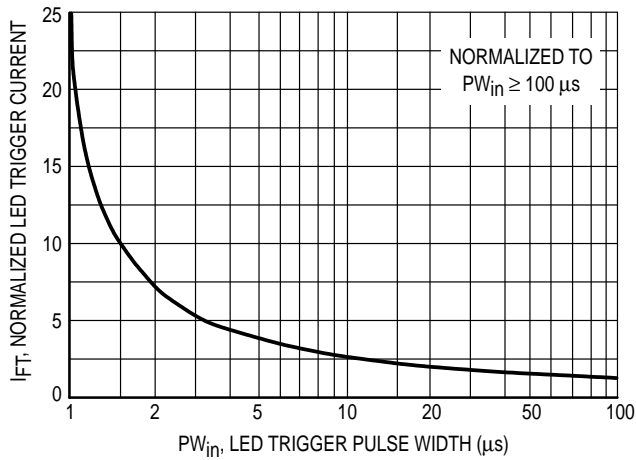


Figure 9. Static dv/dt Test Circuit

TYPICAL ELECTRICAL CHARACTERISTICS

$T_A = 25^\circ\text{C}$



**Figure 10. LED Current Required to Trigger versus LED Pulse Width**

**LED Trigger Current versus PW (Normalized)**

For resistive loads the triac drivers may be controlled by short pulse into the input LED. This input pulse must be synchronized with the AC line voltage zero-crossing points. LED trigger pulse currents shorter than 100 μs must have an increased amplitude as shown on Figure 10. This graph shows the dependency of the trigger current  $I_{FT}$  versus the pulse width  $t(PW)$ .  $I_{FT}$  in the graph,  $I_{FT}$  versus  $(PW)$ , is normalized in respect to the minimum specified  $I_{FT}$  for static condition, which is specified in the device characteristic. The normalized  $I_{FT}$  has to be multiplied with the device's guaranteed static trigger current.

Example:

Guaranteed  $I_{FT} = 10 \text{ mA}$ , Trigger pulse width  $PW = 3.0 \mu\text{s}$   
 $I_{FT}(\text{pulsed}) = 10 \text{ mA} \times 5.0 = 50 \text{ mA}$

**APPLICATIONS GUIDE**

**BASIC APPLICATIONS**

**Basic Triac Driver Circuit**

Zero-cross triac drivers are very immune to static dv/dt. This allows snubberless operations in all applications where the external generated noise amplitude and rate of rise in the AC line is not exceeding the devices' guaranteed limits. For these applications a snubber circuit is not necessary when a noise insensitive power triac is used. Figure 11 shows the circuit diagram. The triac driver is directly connected to the triac main terminal 2 and a series Resistor R which limits the current to the triac driver. Current limiting resistor R could be very small for normal operation since the triac driver can be only switched on within the zero-cross window. Worst case consideration, however, considers accidental turn on at the peak of the line voltage due to a line transient exceeding the devices' maximum ratings. For this reason R should be calculated to limit the current to  $I_{DRM}$  max at the peak of the line voltage.

$$R = V_p AC / I_{TM} \text{ max rep.} = V_p AC / 1A$$

The power dissipation of this current limiting resistor and the triac driver is very small because the power triac carries the load current as soon as the current through driver and current limiting resistor reaches the trigger current of the power triac. The switching transition time for the driver is only one micro second and for power triacs typical four micro seconds.

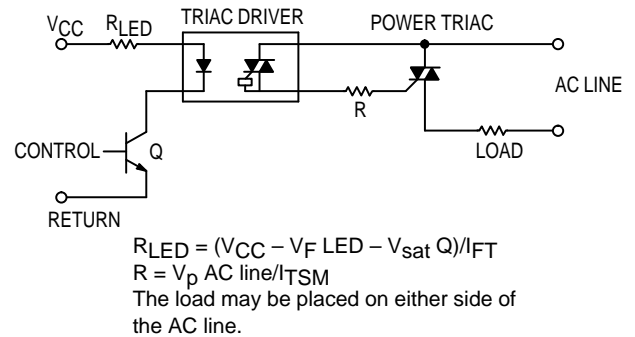
**Triac Driver Circuit for Noisy Environments**

When the transient rate of rise and amplitude are expected to exceed the power triacs and triac drivers maximum ratings a snubber circuit as shown in Figure 12 is recommended. Fast transients are slowed by the R-C snubber and excessive amplitudes are clipped by the Metal Oxide Varistor MOV.

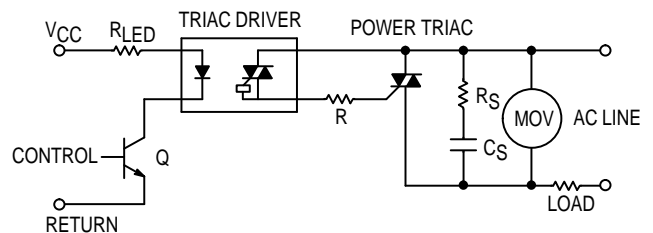
**Triac Driver Circuit for Extremely Noisy Environments**

Noisy environments for this circuit are defined in the noise standards IEEE472, IEC255-4 and IEC801-4.

Industrial control applications, for example, do specify a maximum expected transient noise dv/dt and peak voltage which is superimposed onto the AC line voltage. Figure 13 shows a split snubber network which enhances the circuits noise immunity by protecting the triac driver with optimized efficiency.

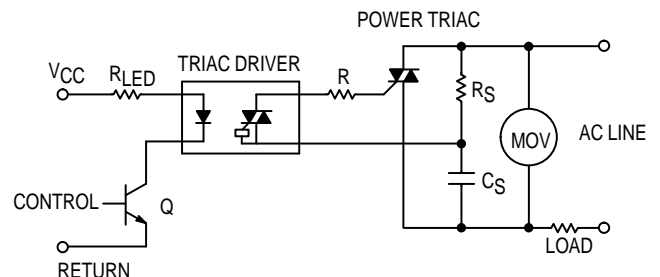


**Figure 11. Basic Driver Circuit**



Traditional snubber configuration  
Typical Snubber values  $R_S = 33 \Omega$ ,  $C_S = 0.01 \mu F$   
MOV (Metal Oxide Varistor) protects triac and driver from transient overvoltages  $>V_{DRM}$  max

**Figure 12. Triac Driver Circuit for Noisy Environments**



Recommended snubber values  $R_S = 10 W$ ,  $C_S = 0.033 mF$

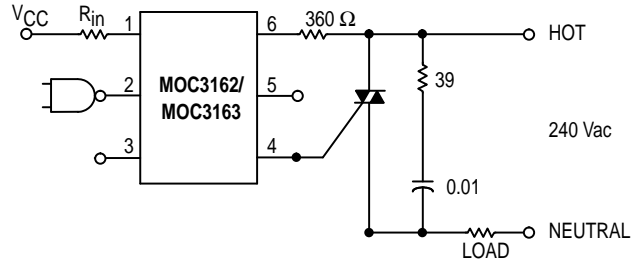
**Figure 13. Triac Driver Circuit for Extremely Noisy Environments**

**APPLICATIONS GUIDE**

**Hot-Line Switching Application Circuit**

Typical circuit for use when hot-line switching is required. In this circuit the "hot" side of the line is switched and the load connected to the cold or neutral side. The load may be connected to either the neutral or hot-line.

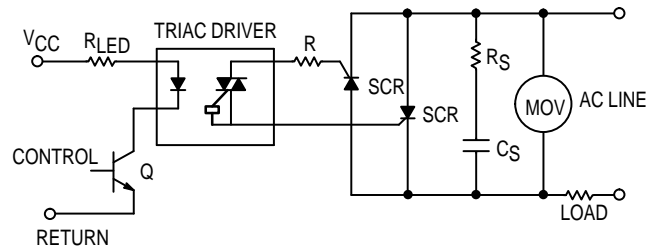
$R_{in}$  is calculated so that  $I_F$  is equal to the rated  $I_{FT}$  of the part, 10 mA for the MOC3162, and 5.0 mA for the MOC3163. The 39 ohm resistor and 0.01  $\mu F$  capacitor are for snubbing of the triac and may or may not be necessary depending upon the particular triac and load used.



**Figure 14. Hot-Line Switching Application Circuit**

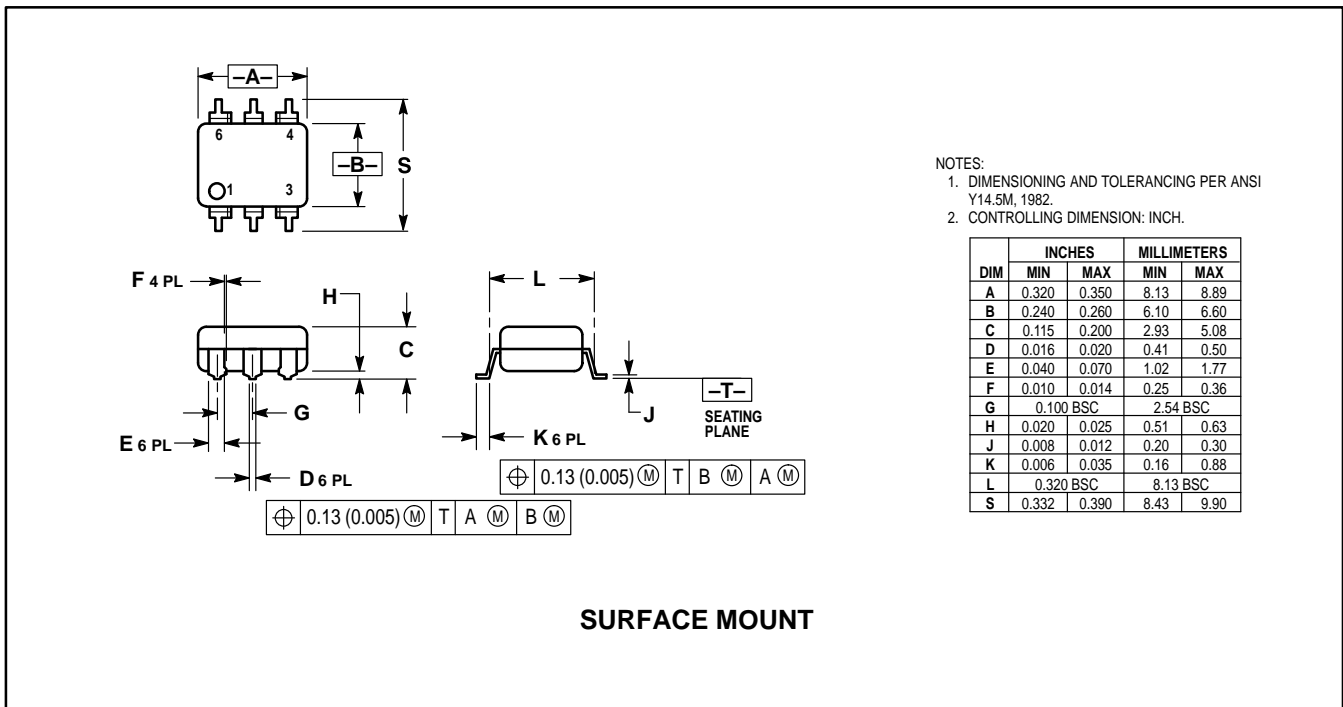
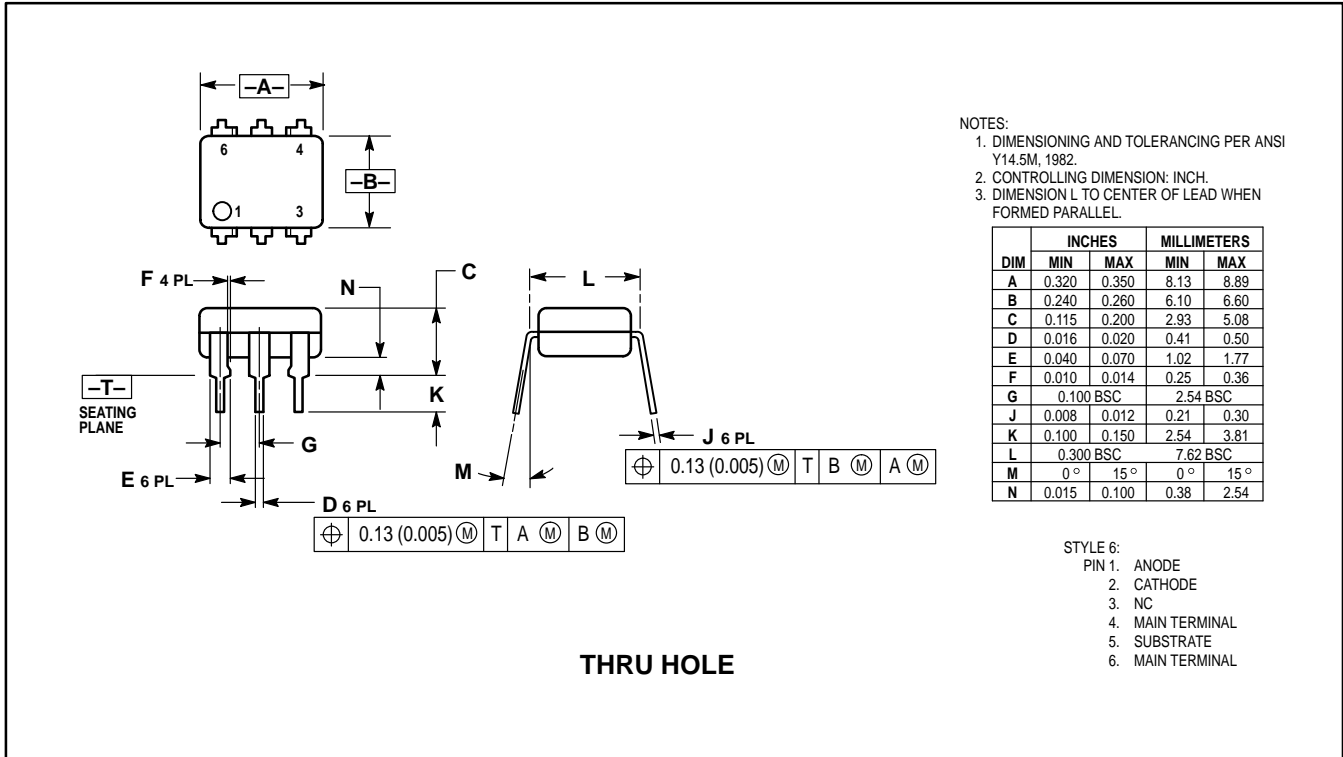
**Inverse Parallel SCR Driver Circuit**

Two inverse parallel SCR's are controlled by one triac driver with a minimum component count as shown in Figure 15. A snubber network and a MOV across the main terminals of the SCR's protects the semiconductors from transients on the AC line.

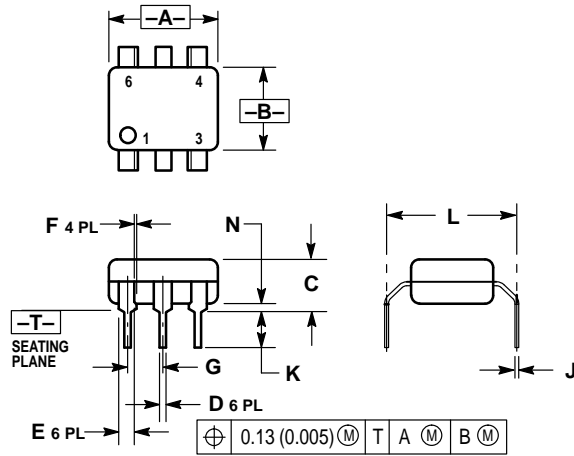


**Figure 15. Inverse Parallel SCR Driver Circuit**

**PACKAGE DIMENSIONS**







- NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
 2. CONTROLLING DIMENSION: INCH.  
 3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.

**0.4" LEAD SPACING**

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## MOC3162-M

## 6-Pin 600V Zero Crossing Triac Driver Output Coupler

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They are designed for use with a triac in the interface of logic systems to equipment powered from 115/240 Vac lines, such as solid state relays, industrial controls, motors, solenoids and consumer appliances, etc.

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## Features

- Simplifies logic control of 115/240 Vac power
- Zero voltage turn-on
- $dv/dt$  of 1000 V/ $\mu$ s guaranteed minimum @600 V peak
- $I_{FT}$  insensitive to static  $dv/dt$  (within rated  $V_{DRM}$ )

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## Applications

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Recommended for 115/240 Vac (rms)

- Solenoid/Valve Controls
- Lighting controls
- Static power switches
- AC motor drives
- Static AC power switch
- Temperature controls
- E.M. contractors
- AC motor starters
- Solid state relays

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Ordering information

- To order devices that are tested and marked per VDE 0884 requirements, the suffix "V" must be included at end of part number. VDE 0884 is a test option.

The following options can be ordered with this part:

Option	Order Entry Identifier	Description
F	F	Low profile, surface mount
S	S	Surface mount
T	T	0.4" Lead bend
V	V	VDE 0884
FV	FV	Low profile, surface mount; VDE 0884
SV	SV	Surface mount; VDE 0884
TV	TV	0.4" Lead bend; VDE 0884
FR2	FR2	Low profile, surface mount; T&R
FR2V	FR2V	Low profile, surface mount; T&R; VDE 0884
SR2	SR2	Surface mount; T&R
SR2V	SR2V	Surface mount; T&R; VDE 0884

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Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
MOC3162F-M	Full Production	\$0.86	N/A	N/A	RAIL
MOC3162FR2-M	Full Production	\$0.87	DIP	6	TAPE REEL
MOC3162FR2V-M	Full Production	\$0.87	DIP	6	TAPE REEL
MOC3162FV-M	Full Production	\$0.86	N/A	N/A	RAIL

MOC3162-M	Full Production	\$0.84	N/A	N/A	RAIL
MOC3162S-M	Full Production	\$0.84	N/A	N/A	RAIL
MOC3162SR2-M	Full Production	\$0.85	DIP	6	TAPE REEL
MOC3162SR2V-M	Full Production	\$0.85	DIP	6	TAPE REEL
MOC3162SV-M	Full Production	\$0.84	DIP	6	RAIL
MOC3162T-M	Full Production	\$0.84	N/A	N/A	RAIL
MOC3162TV-M	Full Production	\$0.84	N/A	N/A	RAIL
MOC3162V-M	Full Production	\$0.84	N/A	N/A	RAIL

\* 1,000 piece Budgetary Pricing

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<a href="#">CR/0117</a> (424 K)	BABT	British Approvals Board of Telecommunications
<a href="#">102497</a> (1629 K)	VDE	VDE Pruf-und Zertifizierungsinstitut
<a href="#">1113639</a> (111 K)	CSA	Canadian Standards Association
<a href="#">0134082</a> (136 K)	SEMKO	SEMKO
<a href="#">FI 17434</a> (47 K)	FIMKO	FIMKO
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## MOC3163-M

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## Applications

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TV	TV	0.4" Lead bend; VDE 0884
FR2	FR2	Low profile, surface mount; T&R
FR2V	FR2V	Low profile, surface mount; T&R; VDE 0884
SR2	SR2	Surface mount; T&R
SR2V	SR2V	Surface mount; T&R; VDE 0884

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Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
MOC3163F-M	Full Production	\$0.86	N/A	N/A	RAIL
MOC3163FR2-M	Full Production	\$0.89	DIP	6	TAPE REEL
MOC3163FR2V-M	Full Production	\$0.89	DIP	6	TAPE REEL
MOC3163FV-M	Full Production	\$0.86	N/A	N/A	RAIL

MOC3163-M	Full Production	\$0.86	N/A	N/A	RAIL
MOC3163S-M	Full Production	\$0.86	N/A	N/A	RAIL
MOC3163SR2-M	Full Production	\$0.87	DIP	6	TAPE REEL
MOC3163SR2V-M	Full Production	\$0.87	DIP	6	TAPE REEL
MOC3163SV-M	Full Production	\$0.86	DIP	6	RAIL
MOC3163T-M	Full Production	\$0.86	N/A	N/A	RAIL
MOC3163TV-M	Full Production	\$0.86	N/A	N/A	RAIL
MOC3163V-M	Full Production	\$0.86	N/A	N/A	RAIL

\* 1,000 piece Budgetary Pricing

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Safety agency certificates

<b>Certificate</b>	<b>Agency</b>	
<a href="#">310983-01</a> (95 K)	DEMKO	DEMKO Testing & Certification
<a href="#">P01101866</a> (383 K)	NEMKO	NEMKO
<a href="#">CR/0117</a> (424 K)	BABT	British Approvals Board of Telecommunications
<a href="#">102497</a> (1629 K)	VDE	VDE Pruf-und Zertifizierungsinstitut
<a href="#">1113639</a> (111 K)	CSA	Canadian Standards Association
<a href="#">0134082</a> (136 K)	SEMKO	SEMKO
<a href="#">FI 17434</a> (47 K)	FIMKO	FIMKO
<a href="#">E90700, Vol. 2</a> (254 K)	UL	Underwriters Laboratories Inc.

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## Former Motorola Products Now Supplied by Fairchild

Select a product number to download its datasheet in PDF format ([Adobe Acrobat Reader](#) required). A -M suffix indicates a former Motorola product.

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<a href="#">4N27-M</a>	<a href="#">4N28-M</a>	4N29-M replaced by <a href="#">4N29</a>
4N29A-M replaced by <a href="#">4N29</a>	4N30-M replaced by <a href="#">4N30</a>	4N31-M replaced by <a href="#">4N31</a>
4N32-M replaced by <a href="#">4N32</a>	4N33-M replaced by <a href="#">4N33</a>	<a href="#">4N35-M</a>
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<a href="#">H11AV1A-M</a>	<a href="#">H11AV2-M</a>	<a href="#">H11AV2A-M</a>
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